Our Ref.: RamBus D-1

## DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office and citizenship are as stated below, next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled INTEGRATED CIRCUIT I/O USING A HIGH PERFORMANCE BUS INTERFACE the specification of which

XX	is attached hereto.	
<del></del>	was filed on	as
	and was amended on(if applicab	le)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above. I do not know and do not believe that the same was ever known or used in the United States of America before my invention thereof, or patented or described in any printed publication in any country before my invention thereof or more than one year prior to this application, that the same was not in public use or on sale in the United States of America more than one year prior to this application, and that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representatives or assigns more than twelve months prior to this application.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56(a).

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of

Federal Regulations, Section 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	(Filing Date)	(Status-patented pending, abandoned)
(Application Serial No.)	(Filing Date)	(Status-patented pending, abandoned)

I hereby appoint Roger S. Borovoy, Reg. No. 20,193, and David J. Larwood, Reg. No. 33,191, 600 Hansen Way, Suite 100, Palo Alto, California 94306, telephone (415) 856-9411, my attorneys with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected herewith. Please address all correspondence to Mr. Larwood.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full Name	of Sole/First Inventors: <u>Michael Farmwale</u>	<u>i</u>
Residence	Berkeley, California 94705 (City, State)	(Country)
Inventor's	Signature <u>Carlwithal</u> famel	Date Ap. 117, 199
Full Name	of Joint/Second Inventor: Mark Horowitz	<u></u>
Residence	2024 Columbia Street Citizenship Palo Alto, California 94306	U.S.A. (Country)
Inventor's	s Signature Aux Koroni	Date April 17, 1990

		IN	THE	UNITED	STATES	PATENT	AND	TRADEMARK	OFFICE
					(Case	No. P04	3D2C	2)	
In	the	Appl	icat	ion of:				)	
		FAI	RMWAI	LD ET AI				) ) Group	
								) Art 11	lnit.

Serial No: Continuation of 08/798,520 )

Before

Filed: Herewith )

Examiner:

Title: INTEGRATED CIRCUIT I/O USING A

HIGH PERFORMANCE BUS INTERFACE

Assistant Commissioner for Patents Washington, DC 20231

## POWER OF ATTORNEY BY ASSIGNEE, REVOCATION OF ALL PRIOR POWERS OF ATTORNEY AND

## CERTIFICATE UNDER 37 CFR 3.73(b)

Sir:

The undersigned, being empowered to sign this Power of Attorney, Revocation of All Previous Powers of Attorney and Certificate under 37 CFR 3.73(b) on behalf of Rambus, Inc., the assignee of the entire right, title and interest in the above-referenced application, hereby revokes all prior powers of attorney and hereby appoints Neil A. Steinberg, Reg. No. 34.735, with full power of substitution and revocation to prosecute this application and to transact all business before the United States Patent and Trademark Office in the above-referenced application.

Rambus, Inc., formerly a California corporation with a place of business at 4920A El Camino Real, Los Altos, California 94022, certifies that it is the assignee of the entire right, title and interest in the above-referenced patent application by virtue of an

assignment from the inventors, Michael Farmwald and Mark Horowitz. The assignment of the parent patent application (application serial 07/510,898) and all continuing and divisional application no. thereof to Rambus Inc. was filed on April 18, 1990 and recorded in the U.S. Patent and Trademark Office at Reel 5385, Frame 875.

The undersigned has reviewed all the documents in the chain of title of the above-referenced application and, to the best of the undersigned's knowledge and belief, title is in Rambus, Inc., the assignee identified above.

Please direct all correspondence in the above-referenced patent application to:

> Neil A. Steinberg, Esq. 5827 Osceola Road Bethesda, Maryland 20816 Telephone: 301-229-7706 Facsimile: 301-229-5882

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so make are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon.

Date: Nov 19,1998

Vice President

Intellectual Property Rambus Inc.